

**Syllabus for Management Trainees Written Exam**

**General Instructions:**

- ✓ Online examination will be of objective type having multiple choices.
- ✓ Total number of questions is **100** each question carry **One Mark**.
- ✓ Evaluation Criteria
  - Written Test (75% weightage)  
written test will be in two sections (Aptitude 40 marks and Subject Domain 60 marks).
  - Interview (25% weightage)
- ✓ There will be **Negative Marking of 0.25** for every wrong answer.
- ✓ Time duration of Exam is **120 minutes** (2 hours).
- ✓ Test paper will be in **Bilingual Language**.

**Syllabus:**

S. No.	➤ Section
1	➤ Analytical Ability Test ➤ Coding Decoding ➤ Number and Figure Series completion ➤ Analogy
2	➤ Quantitative Aptitude ➤ Percentage ➤ Time and Work ➤ Speed, Time and Distance
3	➤ General Awareness ➤ Current Affairs
4	➤ English Language ➤ Grammar / Preposition ➤ Synonyms

<b>Management Trainee (HR)</b>	➤ Human Resource Management ➤ Principles and practices of Management ➤ HRM concepts and functions, Performance Appraisal, Career Planning, Succession Planning ➤ Training and Development, Management Development ➤ Compensation Management ➤ Organizational Behavior ➤ Industrial Relations ➤ Trade Unions ➤ Labour Legislation
<b>Management Trainee (Finance)</b>	➤ Corporate Accounting ➤ Income Tax ➤ Cost Accounting ➤ Theory and Practice of Auditing ➤ Management Accounting ➤ Advanced Cost Accounting ➤ Cost & Management Audit ➤ Computer Application in Accounting ➤ Taxation Law and Practice ➤ Advanced Financial Accounting
<b>Management Trainee (BD/Projects)</b>	➤ Analog Circuits ➤ Antenna ➤ Circuit Analysis

	<ul style="list-style-type: none"> <li>➤ Communication Engineering</li> <li>➤ Control Systems</li> <li>➤ Digital Circuits</li> <li>➤ Electrical Fundamentals</li> <li>➤ Electromagnetic</li> <li>➤ Electronic Devices</li> <li>➤ IC</li> <li>➤ Logic Design</li> <li>➤ Microprocessors and Microcontroller</li> <li>➤ Microwave</li> <li>➤ Network Theorems</li> <li>➤ Operational Amplifiers</li> <li>➤ Power Electronics</li> <li>➤ Signal Processing</li> <li>➤ Signals and Systems</li> <li>➤ VLSI Design</li> <li>➤ Waveguides</li> </ul>
<p><b>Management Trainee (IT)</b></p>	<ul style="list-style-type: none"> <li>➤ Algorithms</li> <li>➤ Computer Organization</li> <li>➤ Data Structures</li> <li>➤ Database</li> <li>➤ Digital Electronics</li> <li>➤ Networking</li> <li>➤ OS Fundamentals</li> <li>➤ Object Oriented Programming with Java</li> <li>➤ Web Technologies</li> </ul>